1 Problem

In circuit analysis one often considers idealized current and voltage sources that can deliver arbitrarily large power into a resistive load at current $I_0$ and voltage $V_0$, respectively. More realistic sources have limitations to the power they can deliver. Discuss the maximum power that can be delivered by

1. A voltage source $V_0$ with an internal series resistance $R_0$.
2. A current source $I_0$ with a maximum output voltage $V_0$, at which voltage some of the current is shunted past the load (as if the current source has a diode in parallel that conducts only for $V > V_0$).\(^1\)

2 Solution

2.1 Voltage Source with Internal Series Resistance

In case of a load of resistance $R$, the total resistance presented to the voltage source is $R + R_0$, so the current which flows is,

$$I = \frac{V_0}{R + R_0}. \quad (1)$$

The total power delivered by the voltage source into the load resistances is,

$$P = V_0 I = \frac{V_0^2}{R + R_0}. \quad (2)$$

The maximum power delivered by the source is for the case that $R = 0$,

$$P_{\text{max}} = \frac{V_0^2}{R_0}. \quad (3)$$

However, none of this power is delivered to an “external” load, so this case is of little practical interest.

The power delivered to the load is,

$$P_{\text{load}} = I^2 R = \frac{V_0^2 R}{(R + R_0)^2}, \quad (4)$$

\(^1\)This is a simplified model of a silicon-solar-cell power source, which is not well represented by a Norton equivalent circuit with ideal current source and parallel resistance.
which is maximal for \( R = R_0 \),

\[
P_{\text{load, max}} = \frac{V_0^2}{4R_0} = \frac{P_{\text{max}}}{4} \quad (R = R_0).
\]

(5)

In this case, the power consumed by the internal resistance \( R_0 \) is \( P_{\text{internal}} = V_0^2 / 4R_0 \) also, such that the total power delivered by the source is,

\[
P_{\text{total}} = \frac{V_0^2}{2R_0} = 2P_{\text{load, max}} = \frac{P_{\text{max}}}{2} \quad (R = R_0).
\]

(6)

The results (5)-(6) are called the **maximum power transfer theorem**. Setting the load resistance equal to the internal resistance is often called **impedance matching** (even in the DC case as here).

### 2.2 Current Source with Maximum Voltage

If a current source of strength \( I_0 \) has a maximum output voltage \( V_0 \), it is useful to define a characteristic impedance \( R_0 \) (although this does not correspond to a physical resistance),

\[
R_0 \equiv \frac{V_0}{I_0}.
\]

(7)

If the load resistance \( R \) is less than \( R_0 \), then the voltage drop across the load is,

\[
V_{\text{load}} = I_0 R < V_0 \quad (R < R_0),
\]

(8)

and the power delivered by the current source to the load is,

\[
P_{\text{load}} = V_{\text{load}}I_0 = I_0^2 R < V_0 I_0 \quad (R < R_0).
\]

(9)

On the other hand, if \( R > R_0 \), current \( I_0 - I \) is shunted past the load (and ideally delivers no power), such that the voltage drop across the load is,

\[
V_{\text{load}} = IR = V_0 \quad (R > R_0).
\]

(10)

The power delivered to the load (= total power delivered) is

\[
P_{\text{load}} = I^2 R = V_0 I = V_0 I_0 \frac{R}{R_0} < V_0 I_0 \quad (R > R_0).
\]

(11)

The two cases (9) and (11) indicate that the maximum power that can be delivered to the load by the current source occurs when \( R = R_0 \) (which can be regarded as a kind of impedance matching),

\[
P_{\text{max}} = V_0 I_0 = I_0^2 R_0 = \frac{V_0^2}{R_0} \quad (R = R_0).
\]

(12)

For \( R > R_0 = V_0/I_0 \) the current source of strength \( I_0 \) considered here behaves like an ideal voltage source of strength \( V_0 \). However, for \( R < R_0 \) where \( V_{\text{load}} = I_0 R < V_0 \), the current source behaves differently than a voltage source.
A Appendix: Maximum Power with a DC-DC Converter

It is sometimes convenient to alter the effective output current and voltage of DC power sources by a DC-DC converter, which is a kind of transformer (with internal time-dependent components) that ideally relates its input and output parameters according to,

\[ V_{\text{out}} = nV_{\text{in}}, \quad I_{\text{out}} = \frac{I_{\text{in}}}{n}, \quad P_{\text{out}} = V_{\text{out}}I_{\text{out}} = V_{\text{in}}I_{\text{in}} = P_{\text{in}}, \]  

(13)

where \( n \) is some positive real number. Here, we verify that use of an ideal DC-DC converter does not change the maximum power that can be delivered by the power sources considered above, although the value of the load resistor for maximum power changes from \( R_0 \) to \( n^2 R_0 \).

A.1 Voltage Source with DC-DC Converter

The power delivered by the DC-DC converter to the load resistor \( R \) is,

\[ P_{\text{out}} = \frac{V_{\text{out}}^2}{R} = \frac{n^2V_{\text{in}}^2}{R} = P_{\text{in}} = V_{\text{in}}I_{\text{in}}, \]  

(14)

such that

\[ n^2V_{\text{in}} = I_{\text{in}}R. \]  

(15)

The input current to the DC-DC converter, when fed by a voltage source \( V_0 \) with a series resistance \( R_0 \) is,

\[ V_{\text{in}} = V_0 - I_{\text{in}}R_0, \]  

(16)

so eq. (15) tells us that,

\[ I_{\text{in}} = \frac{n^2V_0}{R + n^2R_0}, \quad V_{\text{in}} = \frac{V_0 R}{R + n^2R_0}. \]  

(17)

The power into the load resistor \( R \) connected to the output of the DC-DC converter is then,

\[ P_{\text{out}} = \frac{n^2V_{\text{in}}^2}{R} = \frac{n^2V_0^2R}{(R + n^2R_0)^2}, \quad P_{\text{out, max}} = \frac{V_0^2}{4R_0}, \]  

(18)

when \( R = n^2 R_0 \).

Hence, the maximum power into the load and is the same as that found in eq. (5) with no DC-DC converter.

A.2 Solar-Cell Current Source with DC-DC Converter

The solar cell has output current \( I_0 \) with maximum voltage \( V_0 \). As before, we define the characteristic resistance \( R_0 = V_0/I_0 \) (although this is not a physical resistor).

If the load \( R \) on the DC-DC converter is such that \( V_{\text{in}} < V_0 \), we suppose that the input voltage \( V_{\text{in}} \) is established by a physical resistor \( R_1 \) in parallel with the DC-DC converter (on
the input side). For maximum power into the load, this resistor should not consume much power, so we anticipate that \( R_1 \) is large.

The current through resistor \( R_1 \) is,

\[
I_1 = \frac{V_{in}}{R_1},
\]

such that the input current to the DC-DC converter is,

\[
I_{in} = I_0 - I_1 = I_0 - \frac{V_{in}}{R_1}, \quad V_{in} = (I_0 - I_{in})R_1.
\]

The power into the load resistor \( R \) connected to the output of the DC-DC converter is again related by eqs. (14)-(15), so that,

\[
n^2V_{in} = n^2(I_0 - I_{in})R_1 = I_{in}, \quad I_{in} = \frac{n^2I_0R_1}{R + n^2R_1}, \quad V_{in} = \frac{I_0RR_1}{R + n^2R_1}.
\]

The power into the load resistor \( R \) connected to the output of the DC-DC converter is then,

\[
P_{out} = \frac{n^2V_{in}^2}{R} = \frac{n^2I_0^2RR_1^2}{(R + n^2R_1)^2}.
\]

In this expression, both \( R \) and \( R_1 \) are unknown. For a given value of \( R \), the output power (22) is maximal for very large \( R_1 \), as anticipated above. Then,

\[
P_{out} \rightarrow \frac{I_0^2R}{n^2}, \quad \text{and} \quad V_{in} \rightarrow \frac{I_0R}{n^2} \quad (R_1 \rightarrow \infty).
\]

The power \( P_{out} \) increases with the value of \( R \), which also increases \( V_{in} \), until \( V_{in} = V_0 \) when \( R = n^2V_0/I_0 = n^2R_0 \), and,

\[
P_{out} = I_0^2R_0, \quad (R = n^2R_0).
\]

If \( R \) is further increased, the input and output voltages of the DC-DC converter do not change, so the power into the load decreases. Hence, the maximum power into the load is given by eq. (24), and is the same as that found in eq. (12) with no DC-DC converter.

This result was obtained assuming that a very large resistance \( R_1 \) is in parallel with the solar cell, although the precise value of this resistor is unimportant. Equivalent circuits of solar cells are typically drawn with such a resistor present, which permits this circuit to be ascribed an open-circuit voltage \( (V_0) \), as desirable in many types of circuit analysis.