

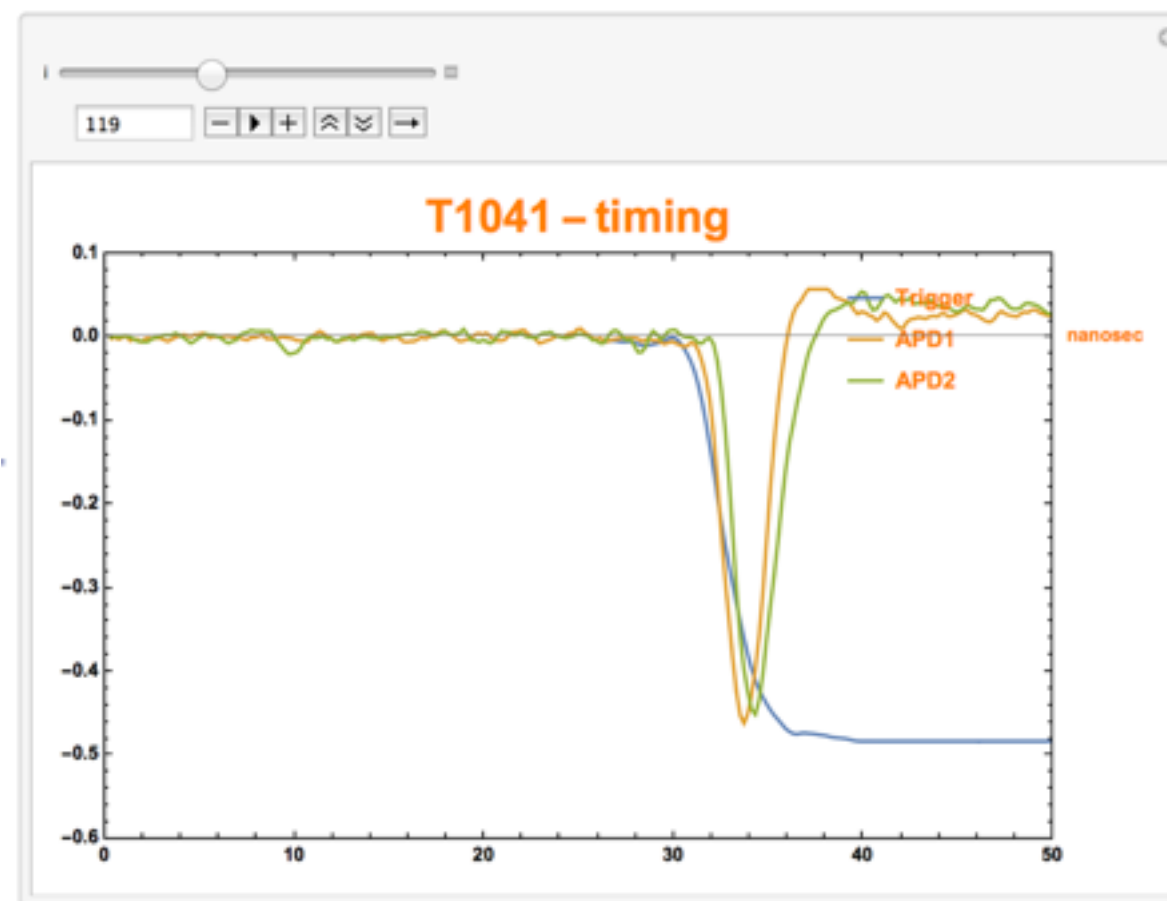
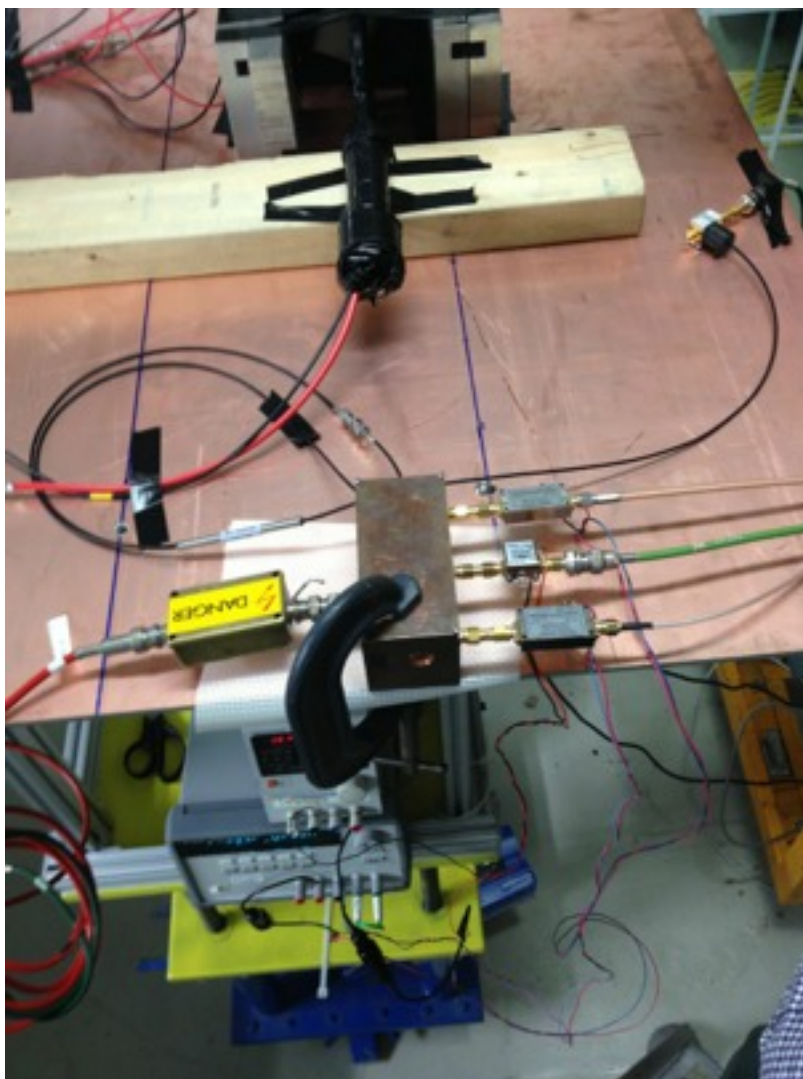
# Common Issues for Fast Timing Measurements

Sebastian White, Rockefeller

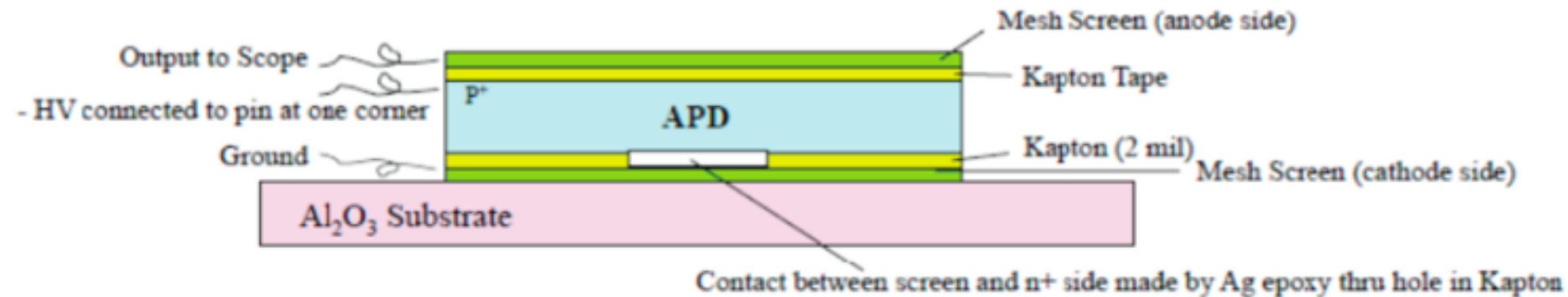
Sept.25,2014

## Much in common with our ongoing work on Si APD charged particle timing

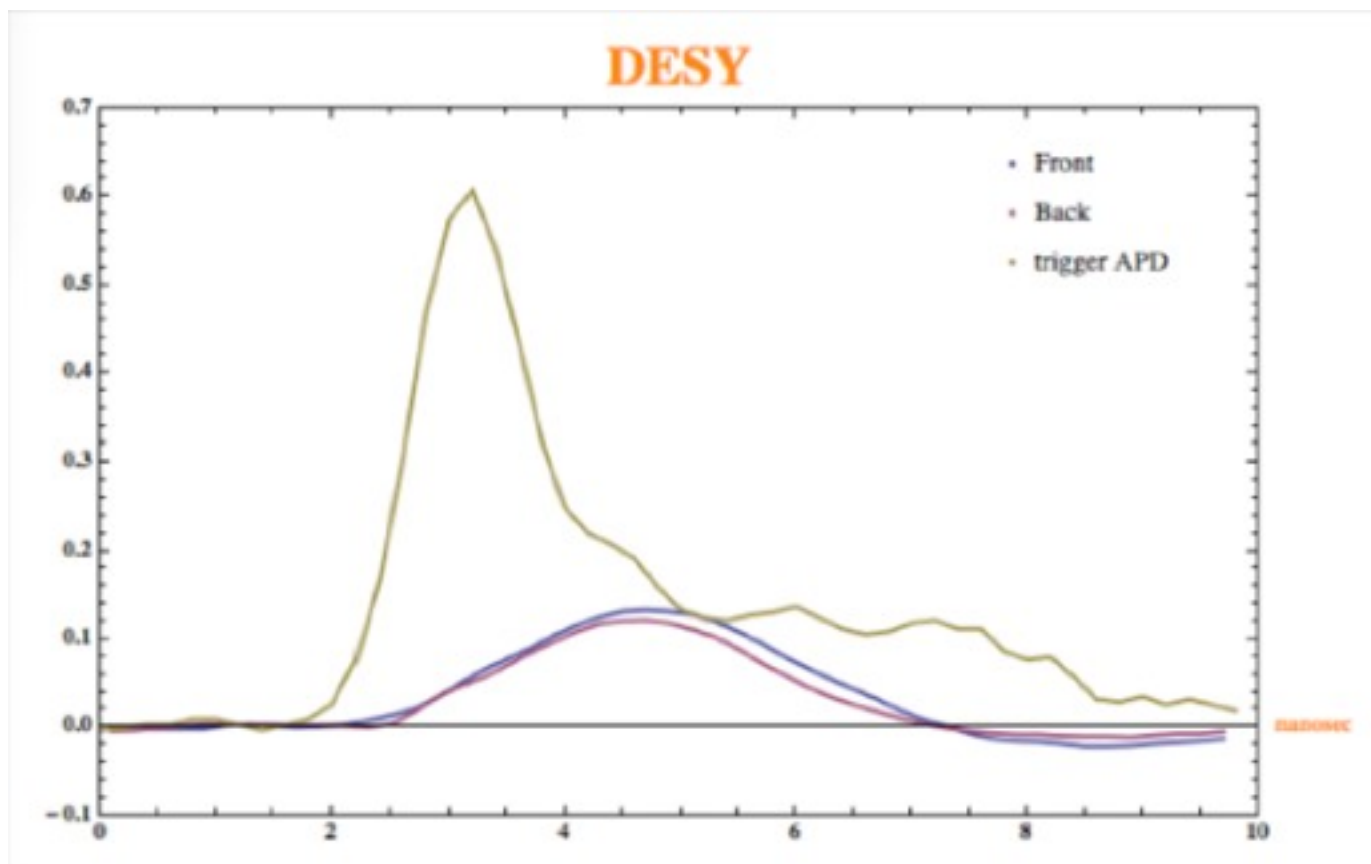
- Detector Area/channel  $\sim 64 \text{ mm}^2$  (suited for HL-LHC occupancy in Endcap)
- $\rightarrow C_D \sim 50\text{-}60 \text{ pF}$
- $\rightarrow$  current limit on  $t_R$  with 50 Ohm preamp ( $\sim 2 \text{ nsec}$  -- about 3 times slower than our low  $C_D$  APDs)



- Much of collaborative development over last 5 years w. RMD/Dynasil on metallization to reduce weighting field non-uniformity and improve high frequency performance=>MicroMegas/APD hybrid structure (eliminate effect of  $R_s$ )



- Main tool for this development was a  $\sim 1000$  nm femtosecond laser with 20 micron spot size
- => detailed response maps over detector area (Amplitude, delay, jitter)
- similar calibration methodology used in all testbeam work
- Thorlabs 980 nm Vcsels driven by fast pulser. Fiber optic splitter/distribution to APDs



most features reproduced in charged particle beams at PSI, CERN, DESY, FNAL

# Modelling effect of large $C_D$

(with J. Kaplon)

## Preamp in voltage mode

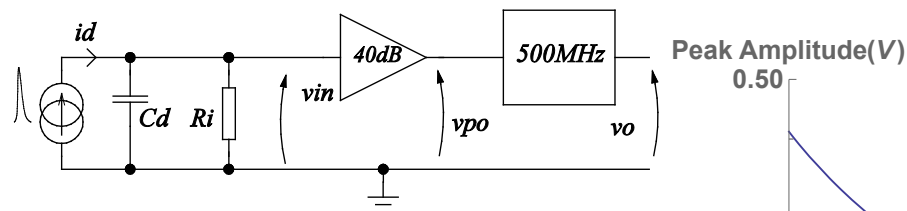


Fig1. Preamplifier working in voltage mode.

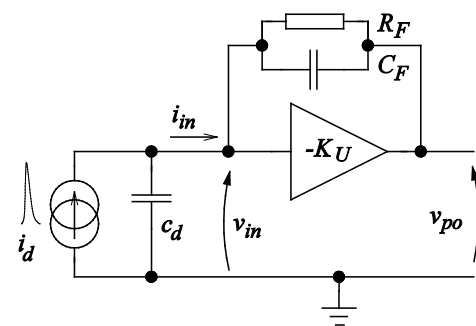
Response  $(v_o(t))$  can be found solving following equations.

Voltages:

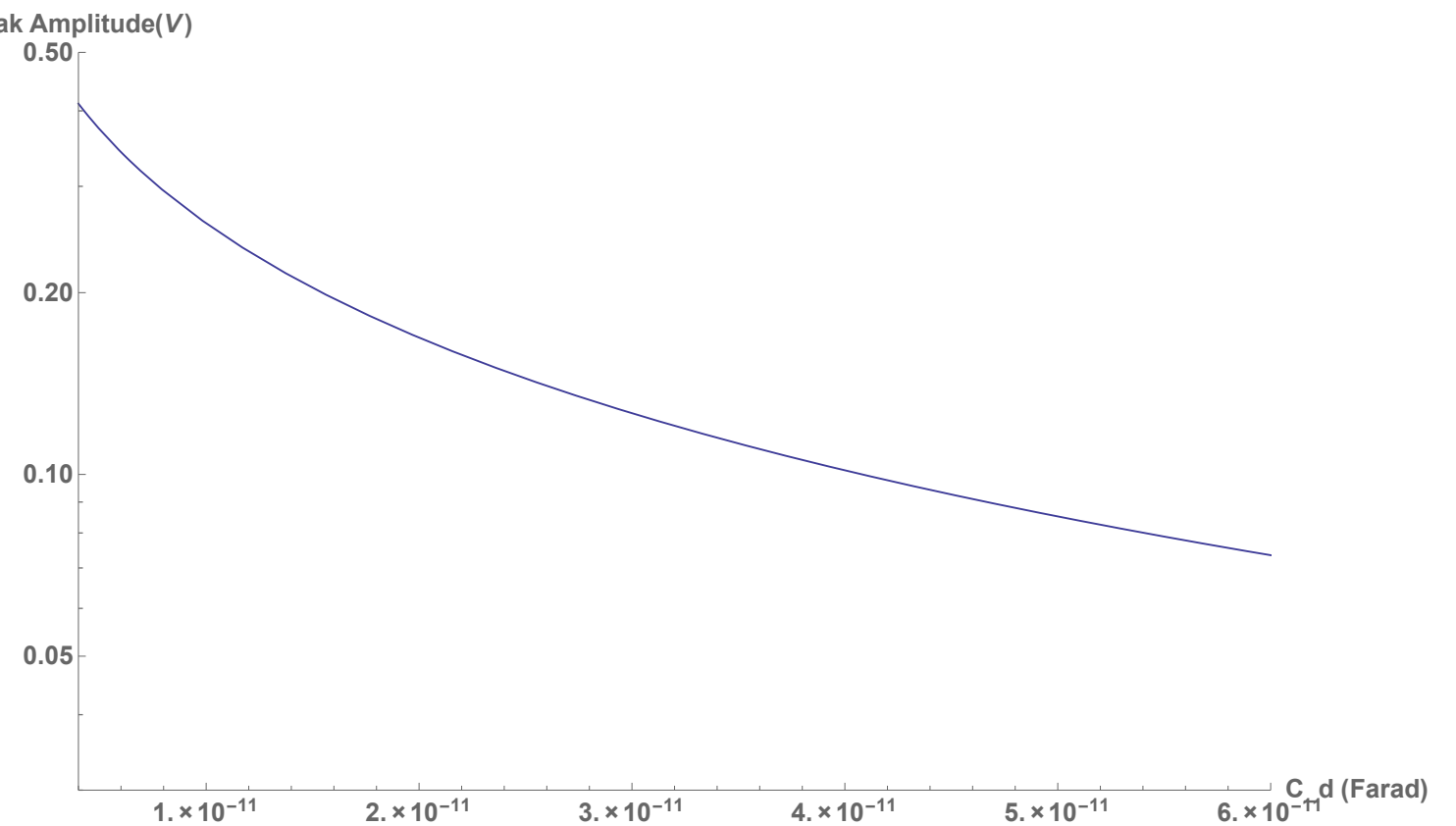
$$v_{in} = i_d \frac{1}{s C_d + \frac{1}{R_i}} = i_d \frac{R_i}{1 + s C_d R_i} \quad v_o = v_{in} K_u(s) = v_{in} \frac{K_u}{1 + s \tau_{p0}}$$

Where  $\tau_{p0}$  defines bandwidth of the amplifier (for 500MHz 3dB bandwidth  $\tau_{p0} = 0.32\text{ns}$ )

## Preamp in charge/transimpedance mode



Assuming high  $K_u$  the amplitude response does not depend in first order on  $c_d$ .



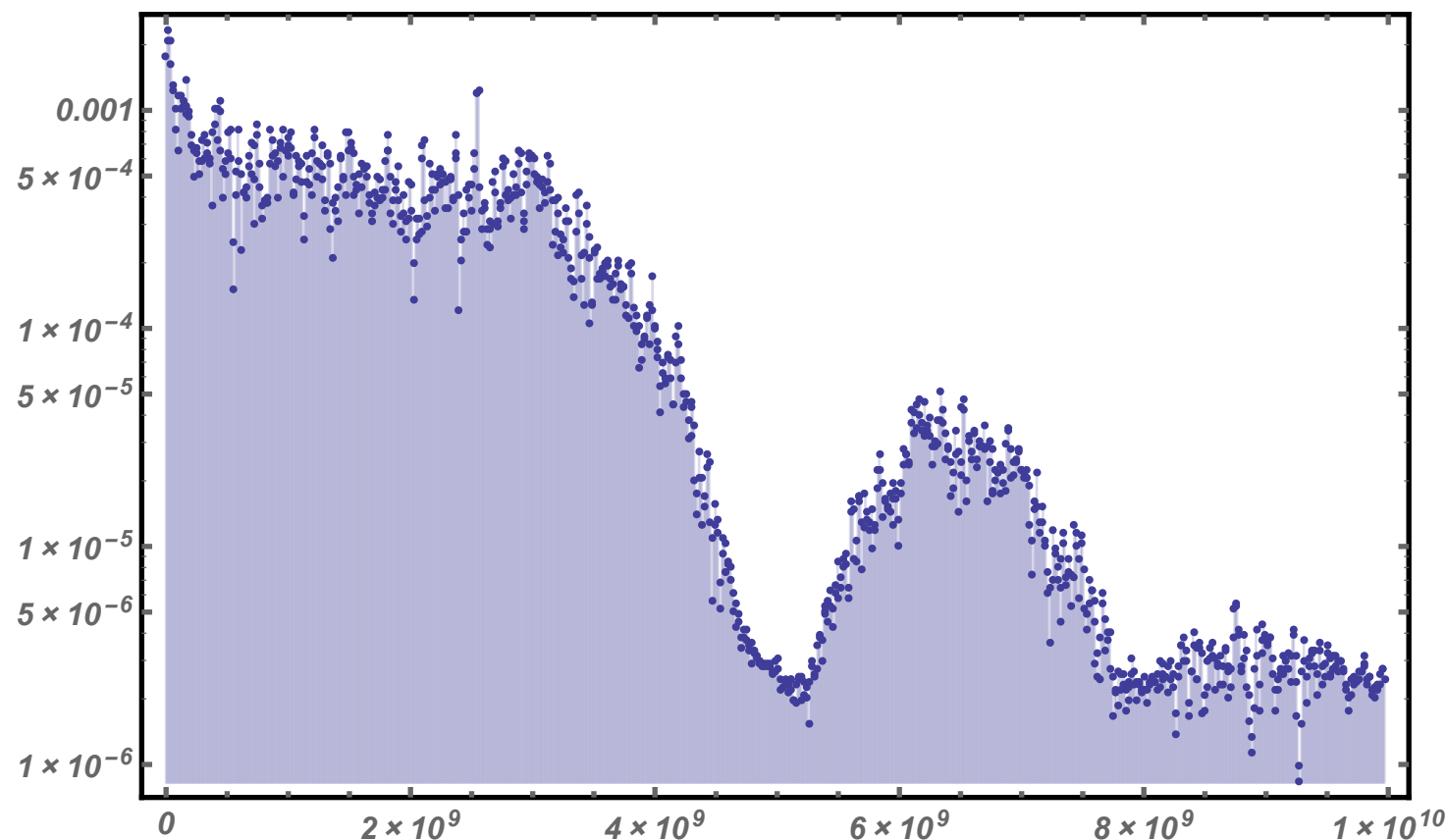
# Performance Specs

- for pileup mitigation need 10-20psec timing (from “bunch crossing time”~170psec)
- what do we expect with present commercial 50 Ohm amps- $\rightarrow t_R \sim 2\text{nsec}$  ?
- roughly given by  $d_T = t_R / \text{SNR}$
- is SNR  $\sim 100:1$  reasonable?
  - signal (40micron, 80e-h micron, APD Gain=520, 50dB preamp)- $\rightarrow 75\text{mV}$
  - noise ( $\sim \text{Sqrt}[4kT(\text{BW})(50\text{Ohm})] * 50\text{dB}$ ) $\sim 4\text{ mV}$
- - $\rightarrow$  need another factor of  $\sim 5$  (new preamps should deliver  $\sim 15$ )

# Meanwhile: Are there tricks to beat time jitter from noise?

- this is key to limits from rad damage effects. Otherwise jitter from leakage current  $\rightarrow 10^{14}$  n lifetime
- mostly trial and error. Use bandpass filtering, Wiener filter, centroid,.. on digitized waveforms. Not yet much better than simple CF algorithm.

*LRS Waverunner Noise Power Spectrum(4mm<sup>2</sup> APD)*



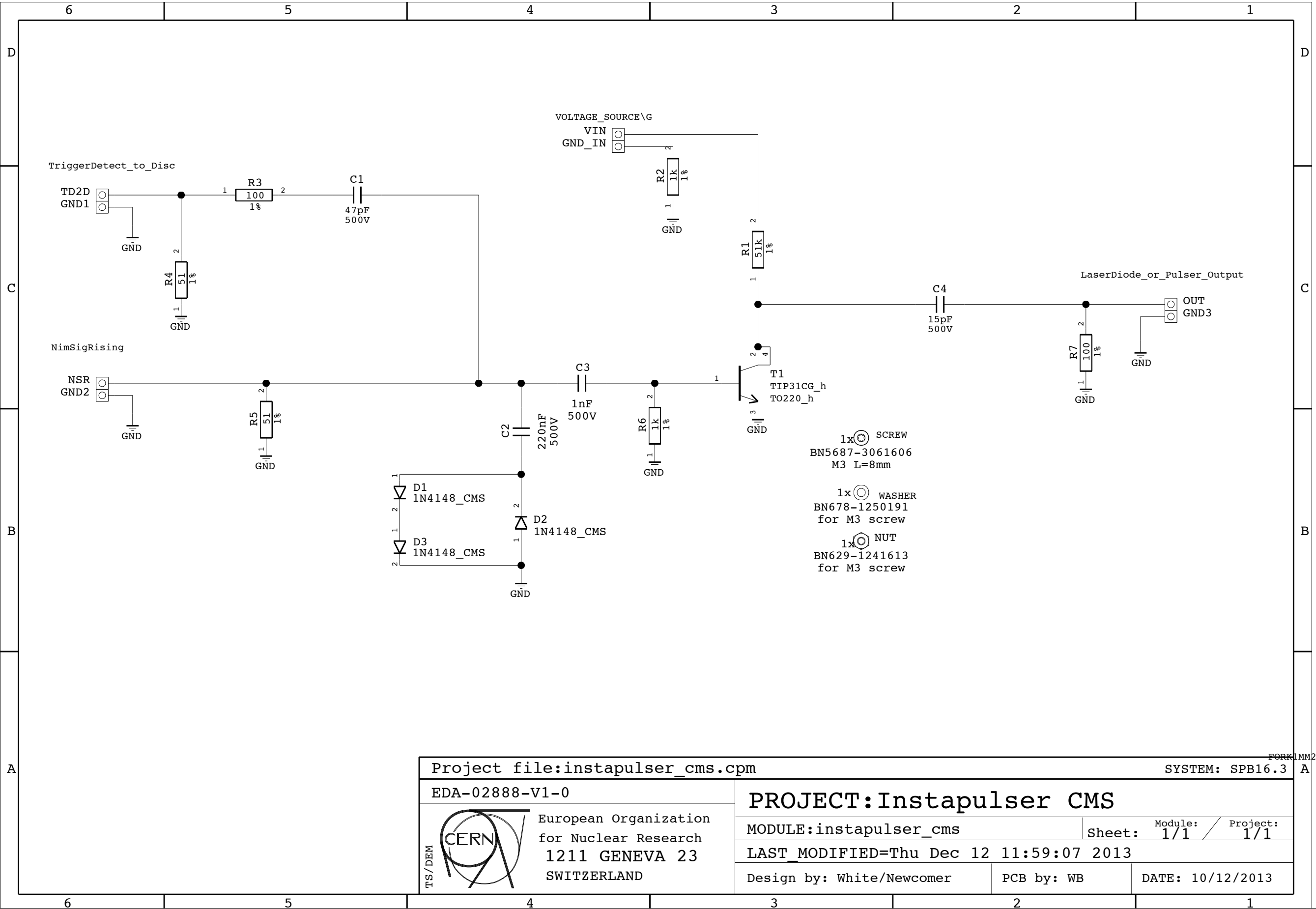
typical noise  
power spectrum

# DAQ/digitizer considerations

- mostly using Waverunner series digital scopes (eg 1 GHz carried by CERN pool), SAMPIC and DRS4v5
- in recent bidding exercise we compared Tektronix, Lecroy, Agilent
- in some respects R&S avoids compromises made by other manufacturers. “digital zoom”, BW limiting on low scales and “interleaving” have sometimes caused problems in the past.
- there’s a nice Labview DAQ tool that runs on all these scopes written by Roman Z.
- wrt Shannon-Nyquist theorem (ie  $f_{\text{sample}} > 2BW_{\text{signal}}$ ) we are always oversampling. This can help w. 8bit limit.

# Test Equipment:Pulsers

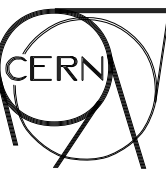
- best we could do with CERN e-pool eqpt. was 3 nsec  $t_R$ , 6nsec width
- <1nsec pulsers often pricey
- so we developed a cheap one “Instapulser-CMS”. PC boards made at CERN paid by A. Ball
- based on driving a transistor close to breakdown, triggered by a  $\sim 1V$  rising edge at base.
- very fast, large amplitude. I normally add a 10pF series capacitor(which limits a bit  $t_R$ ), an inverting transformer and a x20 attenuator --> to get a  $\sim +1.5V$  pulse.



Project file:instapulser\_cms.cpm

EDA-02888-V1-0

TS/DEM



European Organization  
for Nuclear Research  
1211 GENEVA 23  
SWITZERLAND

PROJECT:Instapulser CMS

MODULE:instapulser\_cms

LAST\_MODIFIED=Thu Dec 12 11:59:07 2013

Design by: White/Newcomer

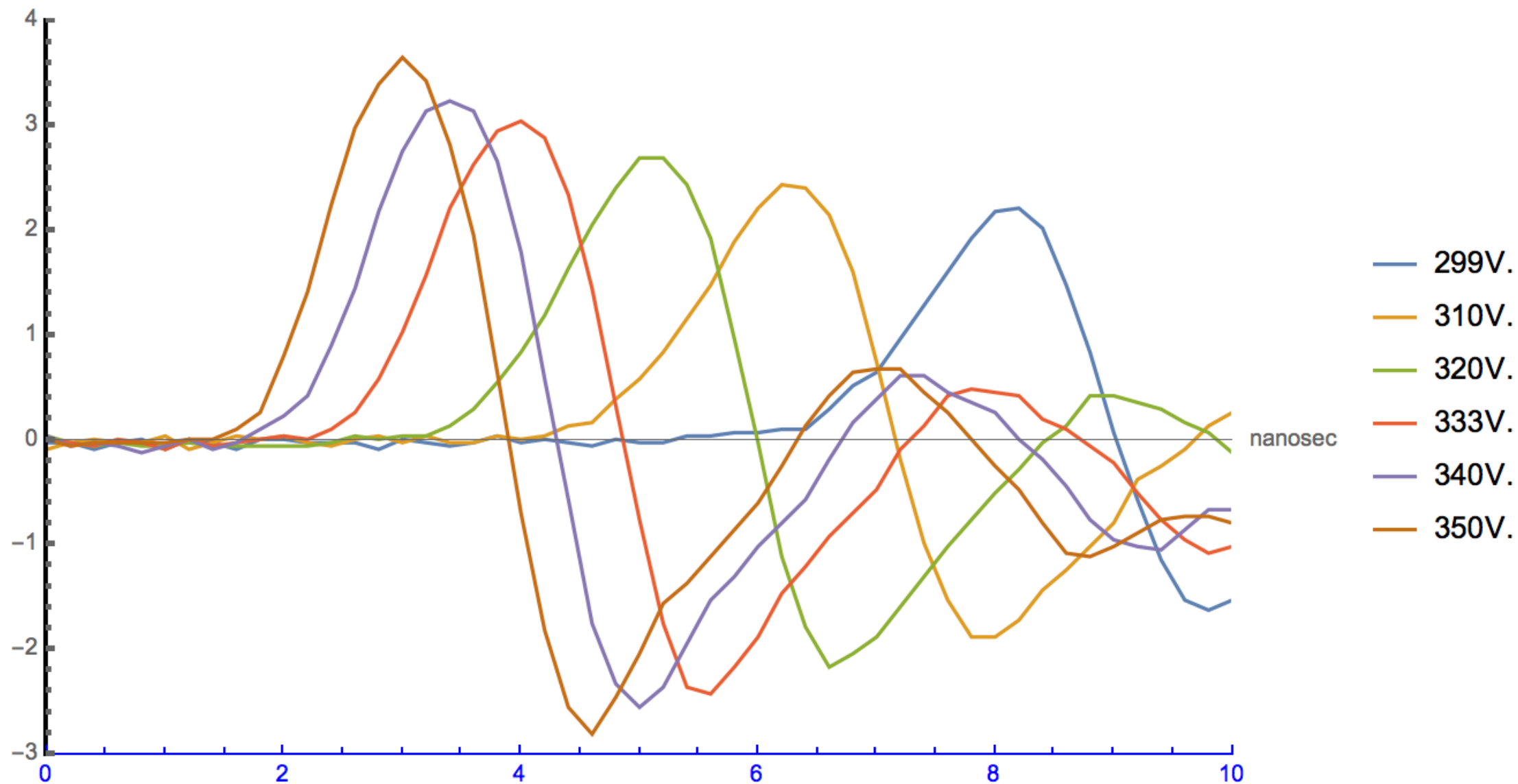
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Sheet: 1/1 / Project: 1/1

DATE: 10/12/2013



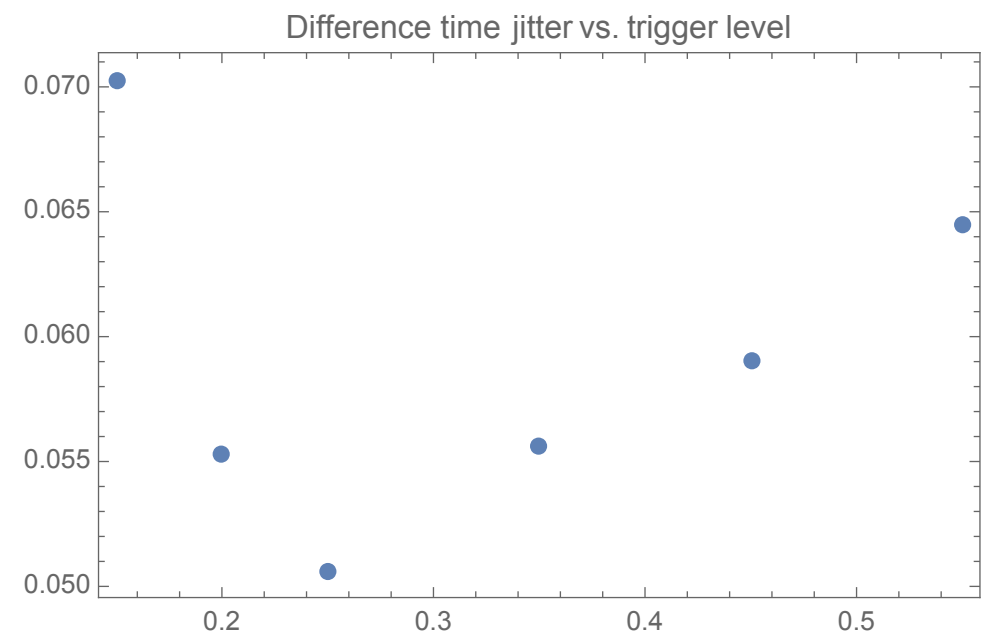
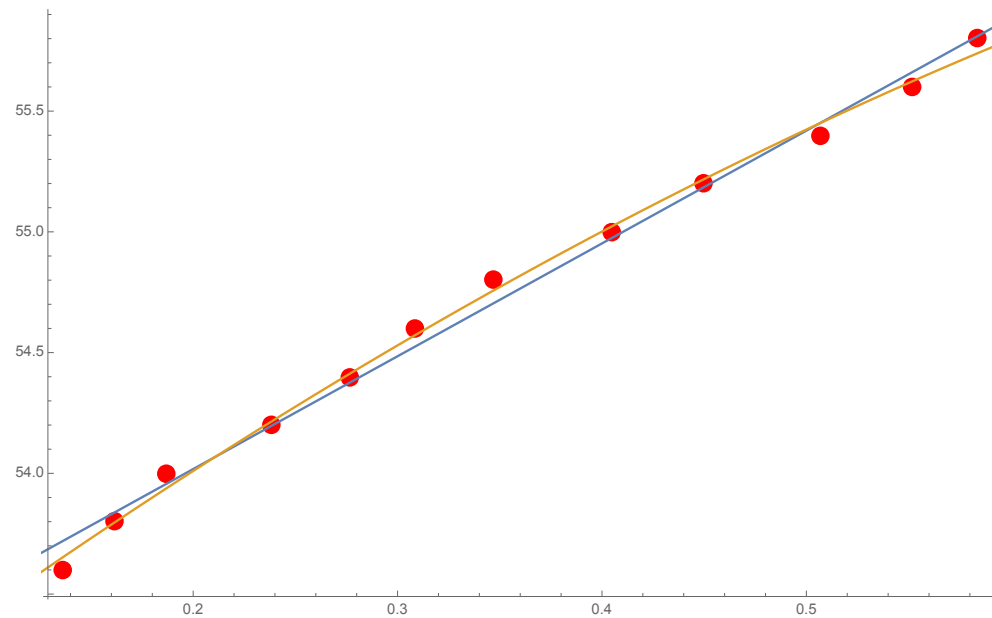
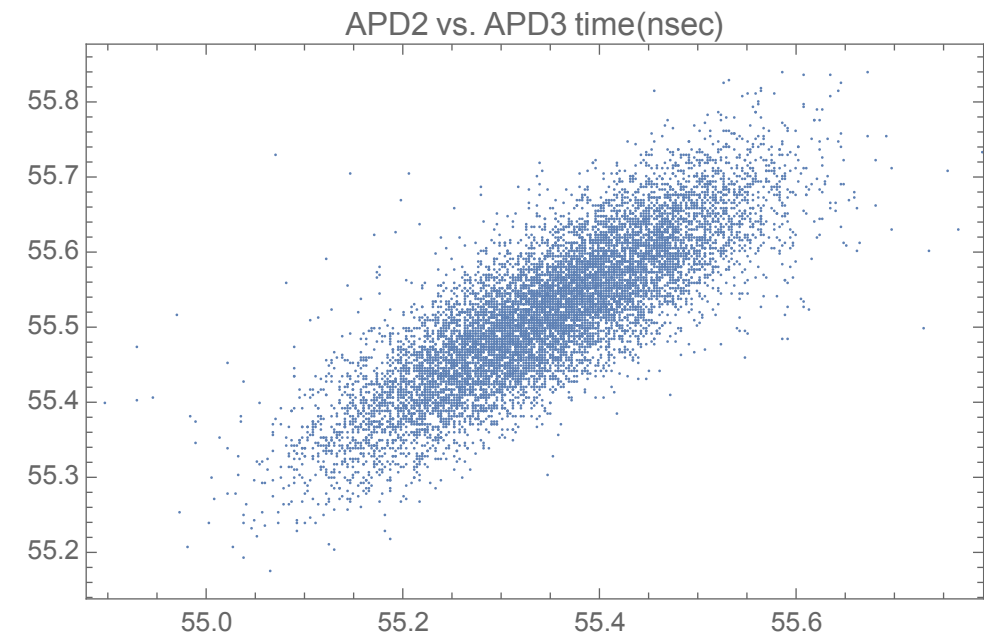
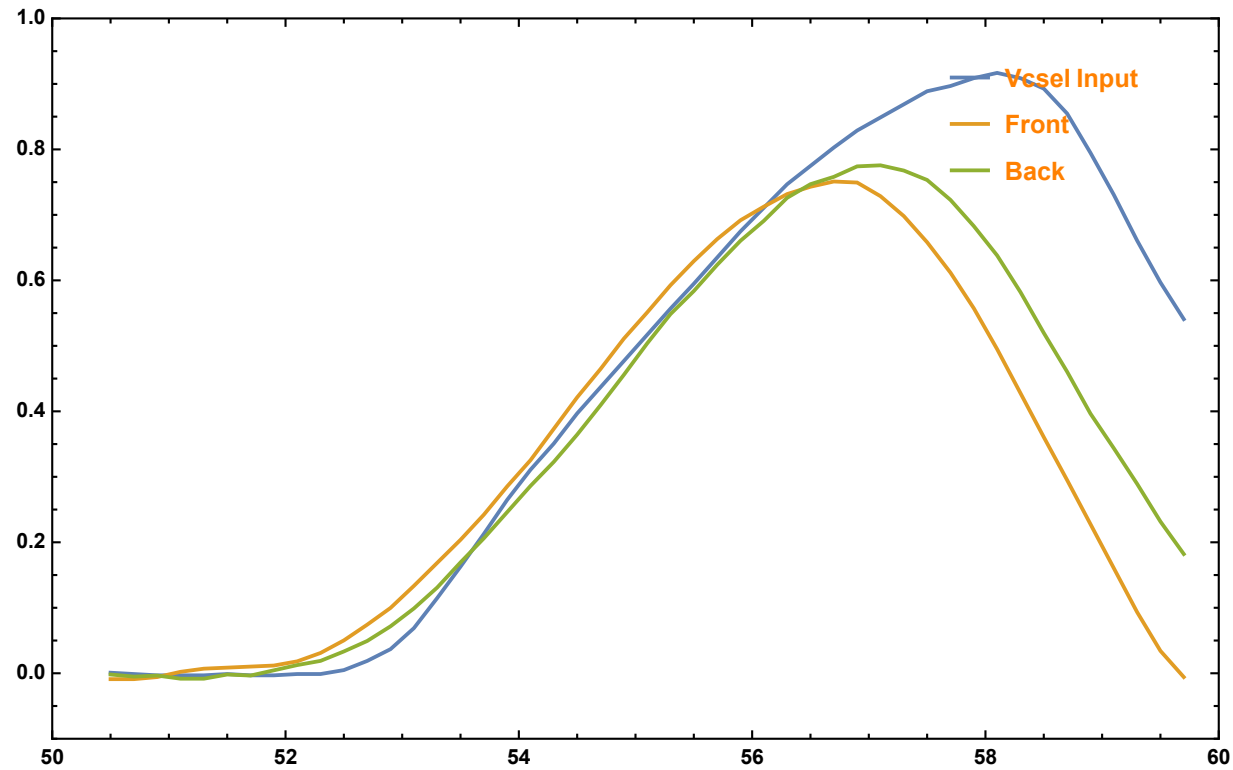
# Instapulser Output pulse vs. $V_{in}$



At 350V, peak=4V(with x10 attenuator!),  $t_R \sim 0.8$ nsec, fine for our application. Could be tweaked w.  $C_{out}$  and  $V_{in}$ .

# tests w. a poor $t_R$

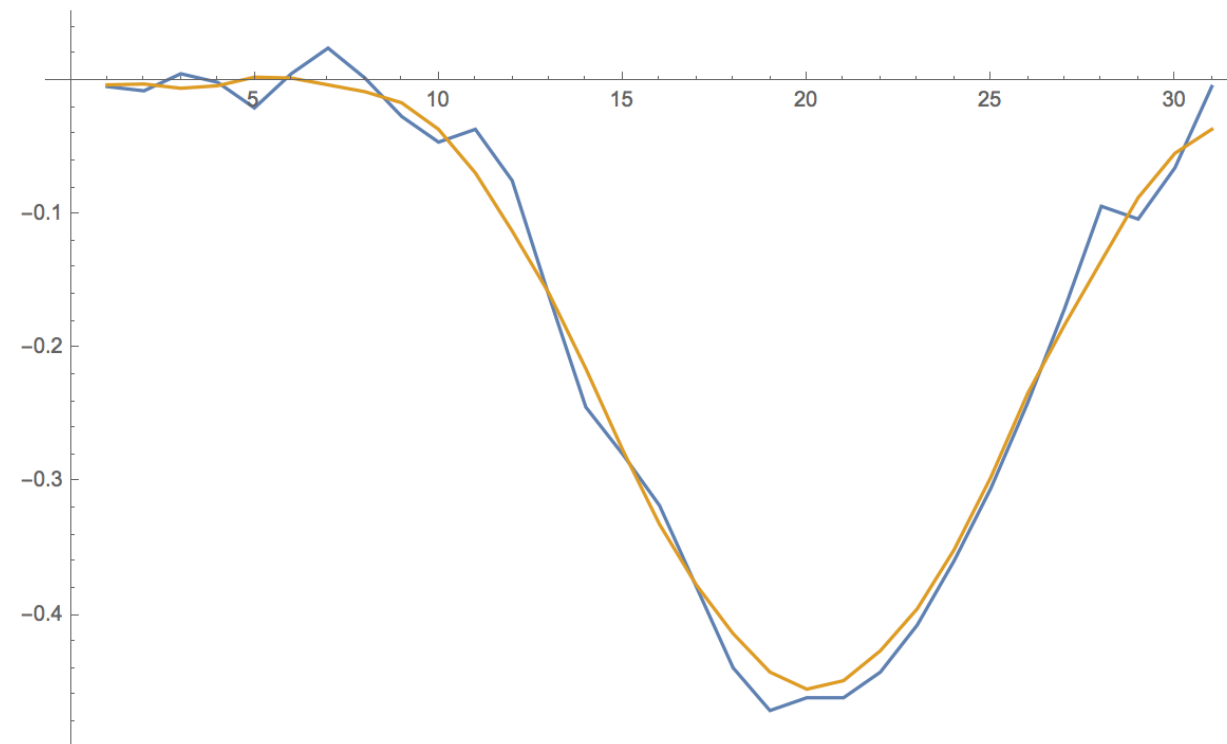
CERN – bench



linear fit method and polynomial  
give similar jitter- $\rightarrow$ ~35psec/detector  
linear less sensitive to threshold.

# return to filtering

```
ListPlot[{wave, WienerFilter[wave, 1.5, .1]}, Joined → True, ImageSize → Large]
```



unoptimized Wiener filter seems effective.  
A signal with 2 nsec  $t_R$  contains no frequencies higher than 200 MHz.

## Near term plans:

- a lot of opportunities for beam testing at CERN in next 2 months.
- Transimpedance amplifier has gone through 3 prototype stages in last month
- original plan to send a telescope to Mitch Newcomer to optimize interconnections
- prefer to do it here
  - meanwhile continue algorithm development using Vcsl and DESY data